Shri Vaishnav Vidyapeeth Vishwavidyalaya **Bachelor of Technology (Computer and Communication Engineering)2016-2017 Choice Based Credit System (CBCS)**

SEMESTER - VIII

S. No.	COURSE CODE	COURSE NAME	Examination Scheme					Teaching Scheme/Week				RKS
			THEORY			PRACTICAL					DITS	MAR
			End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment* (20%)	End Sem University Exam (60%)	Teachers Assessment* (40%)	Th T	Т	P	CRE	TOTAL
1	BTCS801	Project/Internship	0	0	0	180	120	0	0	40	20	300

^{*}Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Chairperson

Shri Vaishnav Vidyapeeth Vishwavidyalaya

Joint Registrar Stell Withhaw Weyspeeth Valuesvidyalay